

Introduction

This poster presents generalized formulation for conversions between electrical parameters matrices. The calculations for two-port devices, presented in [1], are here extended for multiport systems.

Definitions

Submatrices partitioning: The multiport approach consists on considering the S-parameters matrix of the device of interest as a partition of 4 submatrices. Ports are divided in 2 groups: external and internal ports. This kind of partitioning, illustrated here on a S matrix can be applied to Z, Y, h, ABCD and T matrices as well.

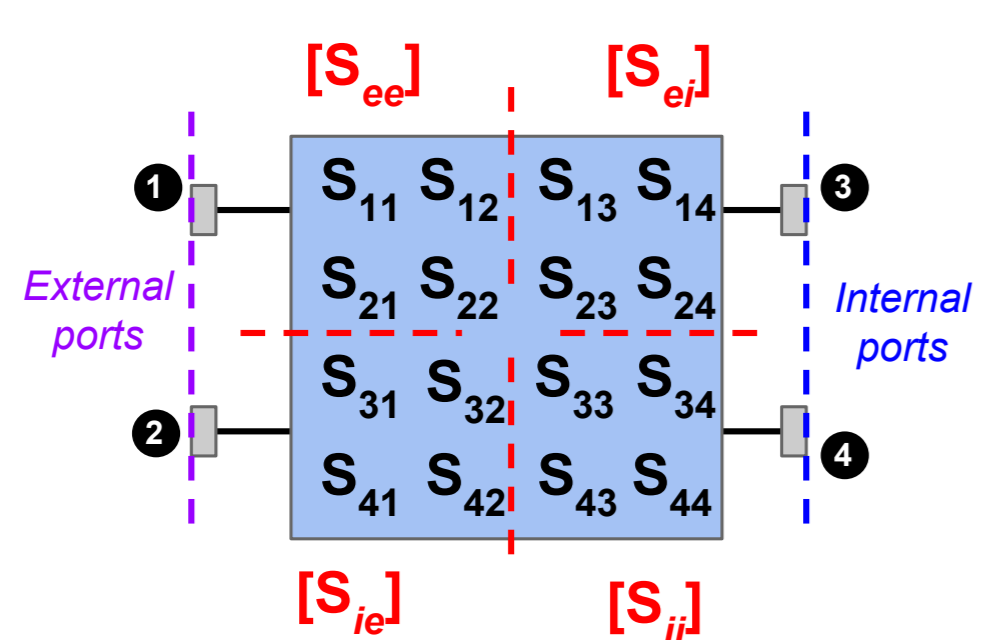


Figure 1: Example of partitioning on a balanced multiport.

Electrical parameters: Matrices defined in [1] can be expanded to multiport as follow:

$$\begin{pmatrix} V_e \\ V_i \end{pmatrix} = \begin{bmatrix} Z_{ee} & Z_{ei} \\ Z_{ie} & Z_{ii} \end{bmatrix} \cdot \begin{pmatrix} I_e \\ I_i \end{pmatrix}$$

$$\begin{pmatrix} I_e \\ I_i \end{pmatrix} = \begin{bmatrix} Y_{ee} & Y_{ei} \\ Y_{ie} & Y_{ii} \end{bmatrix} \cdot \begin{pmatrix} V_e \\ V_i \end{pmatrix}$$

$$\begin{pmatrix} V_e \\ I_i \end{pmatrix} = \begin{bmatrix} h_{ee} & h_{ei} \\ h_{ie} & h_{ii} \end{bmatrix} \cdot \begin{pmatrix} I_e \\ V_i \end{pmatrix}$$

$$\begin{pmatrix} V_e \\ I_e \end{pmatrix} = \begin{bmatrix} A & B \\ C & D \end{bmatrix} \cdot \begin{pmatrix} V_i \\ -I_i \end{pmatrix}$$

$$\begin{pmatrix} b_e \\ b_i \end{pmatrix} = \begin{bmatrix} S_{ee} & S_{ei} \\ S_{ie} & S_{ii} \end{bmatrix} \cdot \begin{pmatrix} a_e \\ a_i \end{pmatrix}$$

$$\begin{pmatrix} a_e \\ b_e \end{pmatrix} = \begin{bmatrix} T_{ee} & T_{ei} \\ T_{ie} & T_{ii} \end{bmatrix} \cdot \begin{pmatrix} b_i \\ a_i \end{pmatrix}$$

Conversions can be calculated according to the power-waves definition by K. Kurokawa [2]:

$$\begin{cases} a_n = \frac{1}{2\sqrt{\Re\{Z_n\}}} \cdot (V_n + Z_n \cdot I_n) \\ b_n = \frac{1}{2\sqrt{\Re\{Z_n\}}} \cdot (V_n - Z_n^* \cdot I_n) \end{cases}$$

where a_n , b_n and Z_n are incident, reflected power waves, and reference impedance at port n respectively.

Matrices Conversions

Electrical parameters matrices:

$$[Y] = [Z]^{-1}$$

$$[Z] = \begin{bmatrix} [A] \cdot [C]^{-1} & [A] \cdot [C]^{-1} \cdot [D] - [B] \\ [C]^{-1} & [C]^{-1} \cdot [D] \end{bmatrix}$$

$$\begin{bmatrix} A & B \\ C & D \end{bmatrix} = \begin{bmatrix} [Z_{ee}] \cdot [Z_{ie}]^{-1} & [Z_{ee}] \cdot [Z_{ie}]^{-1} \cdot [Z_{ii}] - [Z_{ei}] \\ [Z_{ie}]^{-1} & [Z_{ie}]^{-1} \cdot [Z_{ii}] \end{bmatrix}$$

$$[h] = \begin{bmatrix} [Z_{ee}] - [Z_{ei}] \cdot [Z_{ii}]^{-1} \cdot [Z_{ie}] & [Z_{ei}] \cdot [Z_{ii}]^{-1} \\ -[Z_{ii}]^{-1} \cdot [Z_{ie}] & [Z_{ii}]^{-1} \end{bmatrix}$$

$$[Z] = \begin{bmatrix} [h_{ee}] - [h_{ei}] \cdot [h_{ii}]^{-1} \cdot [h_{ie}] & [h_{ei}] \cdot [h_{ii}]^{-1} \\ -[h_{ii}]^{-1} \cdot [h_{ie}] & [h_{ii}]^{-1} \end{bmatrix}$$

$$[Y] = \begin{bmatrix} [D] \cdot [B]^{-1} & [C] - [D] \cdot [B]^{-1} \cdot [A] \\ -[B]^{-1} & [B]^{-1} \cdot [A] \end{bmatrix}$$

$$\begin{bmatrix} A & B \\ C & D \end{bmatrix} = \begin{bmatrix} -[Y_{ie}]^{-1} \cdot [Y_{ii}] & -[Y_{ie}]^{-1} \\ [Y_{ei}] - [Y_{ee}] \cdot [Y_{ie}]^{-1} \cdot [Y_{ii}] & -[Y_{ie}]^{-1} \cdot [Y_{ee}] \end{bmatrix}$$

$$[h] = \begin{bmatrix} [Y_{ee}]^{-1} & -[Y_{ee}]^{-1} \cdot [Y_{ei}] \\ [Y_{ie}] \cdot [Y_{ee}]^{-1} & [Y_{ii}] - [Y_{ie}] \cdot [Y_{ee}]^{-1} \cdot [Y_{ei}] \end{bmatrix}$$

$$[Y] = \begin{bmatrix} [h_{ee}]^{-1} & -[h_{ee}]^{-1} \cdot [h_{ei}] \\ [h_{ie}] \cdot [h_{ee}]^{-1} & [h_{ii}] - [h_{ie}] \cdot [h_{ee}]^{-1} \cdot [h_{ei}] \end{bmatrix}$$

Scattering parameters matrix [S]:

$$[Z] = [G_0]^{-1} \cdot ([I] - [S])^{-1} \cdot ([S] \cdot [Z_0] + [Z_0^*]) \cdot [G_0]$$

$$[S] = [G_0] \cdot ([Z] - [Z_0^*]) \cdot ([Z] + [Z_0])^{-1} \cdot [G_0]^{-1}$$

$$[Y] = [G_0]^{-1} \cdot ([S] \cdot [Z_0] + [Z_0^*])^{-1} \cdot ([I] - [S]) \cdot [G_0]$$

$$[S] = [G_0] \cdot ([I] - [Z_0^*] \cdot [Y]) \cdot ([I] + [Z_0] \cdot [Y])^{-1} \cdot [G_0]^{-1}$$

with

$$[G_0] = \text{diag}\{g_1, \dots, g_n, \dots, g_N\}$$

$$[Z_0] = \text{diag}\{Z_1, \dots, Z_n, \dots, Z_N\}$$

and $[I]$ is the identity matrix. $[G_0]$ and $[Z_0]$ are diagonal matrices (terms outside the diagonal are zero) where each term is related to a port reference impedance Z_n and

$$g_n = \frac{1}{\sqrt{\Re\{Z_n\}}}$$

Transfer parameters matrix [T]:

$$[T] = \begin{bmatrix} [S_{ie}]^{-1} & -[S_{ie}]^{-1} \cdot [S_{ii}] \\ [S_{ee}] \cdot [S_{ie}]^{-1} & [S_{ei}] - [S_{ee}] \cdot [S_{ie}]^{-1} \cdot [S_{ii}] \end{bmatrix}$$

$$[S] = \begin{bmatrix} [T_{ie}] \cdot [T_{ee}]^{-1} & [T_{ii}] - [T_{ie}] \cdot [T_{ee}]^{-1} \cdot [T_{ei}] \\ [T_{ee}]^{-1} & -[T_{ee}]^{-1} \cdot [T_{ei}] \end{bmatrix}$$

Chain matrices $[ABCD]$ and $[T]$ are properly defined only when the system is balanced (same number of internal and external ports). If the system is unbalanced, we have to ensure the uniqueness of the solution and then to apply the pseudo-inverse operator instead of the inverse matrix function.

S-parameters Normalization

The change of reference impedance of a multiport S-parameters $[S]$ matrix from $[Z_0]$ to $[Z'_0]$ is:

$$[S'] = [A]^{-1} \cdot ([S] - [\rho^*]) \cdot ([I] - [\rho] \cdot [S])^{-1} \cdot [A^*]$$

where

$$[A] = [G'_0]^{-1} \cdot [G_0] \cdot ([I] - [\rho^*])$$

$$[\rho] = ([Z'_0] - [Z_0]) \cdot ([Z'_0] + [Z_0^*])^{-1}$$

$[I]$ is the identity matrix, $[G_0]$, $[G'_0]$, $[Z_0]$ and $[Z'_0]$ are previously defined. All matrices, except $[S]$ and $[S']$, are diagonal matrices.

Embedding

The embedding procedure, and SnP reduction, consist on the S-parameters calculation at the external ports according to the perfectly known S-parameters connected at the internal ports. The formula is:

$$[S_{\text{Global}}] = [S_{ee}] + [S_{ei}] \cdot ([I] - [S_{ii}] \cdot [S_{ii}])^{-1} \cdot [S_{ii}] \cdot [S_{ie}]$$

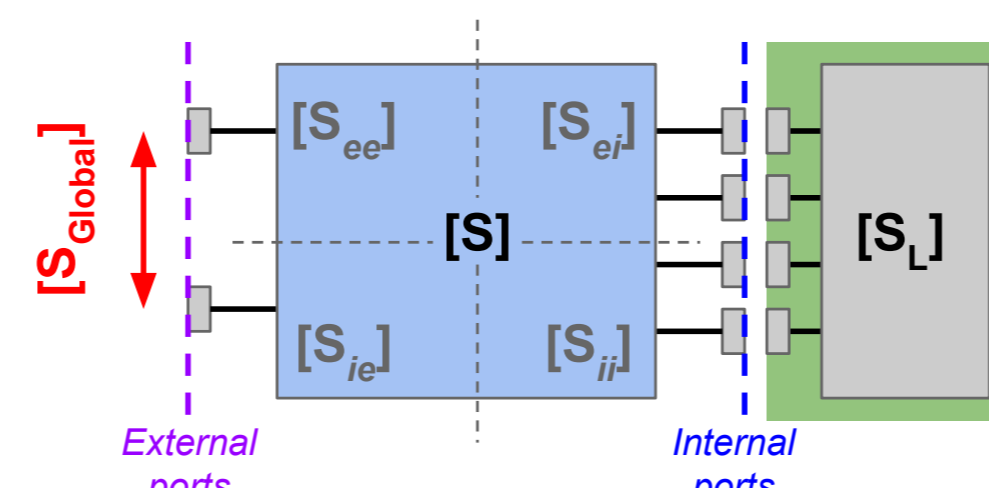


Figure 2: Illustration of an unbalanced multiport S-parameters terminated with a multiport load at its internal ports.

We can apply the same embedding procedure with $[Z]$ or $[Y]$ matrices without any condition on the multiport device:

$$[Z_{\text{Global}}] = [Z_{ee}] - [Z_{ei}] \cdot ([Z_{ii}] + [Z_L])^{-1} \cdot [Z_{ie}]$$

$$[Y_{\text{Global}}] = [Y_{ee}] - [Y_{ei}] \cdot ([Y_{ii}] + [Y_L])^{-1} \cdot [Y_{ie}]$$

De-embedding

The de-embedding procedure consists on extracting the $[S_L]$ when $[S]$ and S_{Global} are known:

$$[S_L] = [S_{ei}]^{-1} \cdot ([S_{\text{Global}}] - [S_{ee}]) \cdot ([S_{ie}] + [S_{ii}] \cdot [S_{ei}]^{-1} \cdot ([S_{\text{Global}}] - [S_{ee}]))^{-1}$$

The equation works only when $[S]$ describes a balanced system. If the number of internal ports (N_i) is lower than the number of external port (N_e), we can use the pseudo-inverse operator to invert S_{ei} and get the least-square solution. Otherwise, the solution can not be solved and requires more assumptions.

Test Fixture	[S]	[T]	[S _L]
	$\begin{bmatrix} 3 \times 3 & 3 \times 3 \\ 3 \times 3 & 3 \times 3 \end{bmatrix}$	$\begin{bmatrix} 3 \times 3 & 3 \times 3 \\ 3 \times 3 & 3 \times 3 \end{bmatrix}$	\times $\begin{bmatrix} 3 \times 3 \\ 3 \times 3 \end{bmatrix}$
	$\begin{bmatrix} 4 \times 4 & 2 \times 2 \\ 2 \times 4 & 2 \times 2 \end{bmatrix}$	$\begin{bmatrix} 4 \times 2 & 4 \times 2 \\ 4 \times 2 & 4 \times 2 \end{bmatrix}$	\times $\begin{bmatrix} 2 \times 2 \\ 2 \times 2 \end{bmatrix}$
	$\begin{bmatrix} 2 \times 2 & 2 \times 4 \\ 2 \times 4 & 4 \times 4 \end{bmatrix}$	$\begin{bmatrix} 2 \times 4 & 2 \times 4 \\ 2 \times 4 & 2 \times 4 \end{bmatrix}$	\times $\begin{bmatrix} 4 \times 4 \\ 4 \times 4 \end{bmatrix}$

This table illustrates a de-embedding on a multiport device. The first line represents a balanced system. The second line is an unbalanced system with $N_e > N_i$. The matrix inversion has to be done with the pseudo-inverse operator. The third line represents an unbalanced system with $N_i > N_e$. There is not uniqueness of the solution for the matrix inversion. Note that S to T matrix transformation is lossy [3] with unbalanced networks when $N_i > N_e$.

Conclusion

This poster presents a multiport extension of the matrix parameters formulas by Frickey in 1994 [1]. The reader can handle, convert, normalize or reduce any matrix representing a linear multiport.

References

- [1] D. A. Frickey, "Conversions between S, Z, Y, H, ABCD, and T parameters which are valid for complex source and load impedances," *IEEE Transactions on Microwave Theory and Techniques*, vol. 42, no. 2, pp. 205–211, February 1994.
- [2] K. Kurokawa, "Power Waves and the Scattering Matrix," *IEEE Transactions on Microwave Theory and Techniques*, vol. 13, no. 2, pp. 194–202, March 1965.
- [3] J. Frei, X.-D. Xiao-Ding Cai, and S. Muller, "Multiport S-Parameter and T-Parameter Conversion With Symmetry Extension," *IEEE Transactions on Microwave Theory and Techniques*, vol. 56, no. 11, pp. 2493–2504, nov 2008.

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